



500.39912CX1
Response Under 37 CFR § 1.116
Expedited Procedure
Art Unit 2813

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. IWASAKI, et al.
Application No.: 10/611,862
Filed: July 3, 2003
For: SEMICONDUCTOR DEVICE WITH MULTILAYER
CONDUCTIVE STRUCTURE FORMED ON A
SEMICONDUCTOR SUBSTRATE
Art Unit: 2813
Examiner: S. W. Smoot

AMENDMENT AFTER FINAL REJECTION

Commissioner for Patents
P.O. BOX 1450
Alexandria, VA 22313-1450

January 12, 2005

Sir:

In response to the Office Action dated September 24, 2004, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Claims and

Remarks follow the amendments.